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GA 2739

JUL 07 2000

PATENT APPLICATION

GROUP 2700

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Ofek et al.

For: A Switching System and
Methodology Having Scheduled
Connection on Input and Output
Ports Responsive to Common
Time Reference

Serial No.: 09/535,831

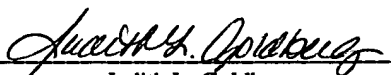
Filed: March 28, 2000

Examiner: Not Yet Assigned

Art Unit: 2739

CERTIFICATE OF MAILING

I hereby certify that this Information Disclosure
Statement is being deposited with the United
States Postal Service with sufficient postage as
First Class Mail in an envelope addressed to:
Assistant Commissioner for Patents, Box DD,
Washington, D.C. 20231 on 6/30/00.


Judith L. Goldberg

INFORMATION DISCLOSURE STATEMENT

BOX DD
Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

Pursuant to 37 C.F.R. §1.97, a list of documents is disclosed on the attached form PTO-1449 that may be material to the examination of this application. A copy of each of the documents is enclosed herewith for the Examiner's consideration.

No inference should be drawn that the attached list represents a comprehensive investigation of the prior art; that any or all are pertinent to the invention; or that any apparatus disclosed is equivalent to the subject invention.

U.S. Patent No.

5,418,779

Inventor

Yemini et al.

Documents:

A. Pattavina, "Switching Theory: Architecture and Performance in Broadband ATM Networks", John Wiley & Sons, NY (1998), table of contents.

Nichols, et al. "Definition of the Differentiated Services Field (DS Field) in the IPv4 and Ipv6 Headers", Network Working Group Request for Comments 2474, December 1998, pp. 1-20.

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- Mills, et al., "Final Report on the Highball Project," Technical Report 95-4-1, University of Delaware, April 1995
- Awdeh, et al., "Survey of ATM Switch Architectures," Computer Networks and ISDN Systems, No. 27, 1995, pages 1567-1613
- Broomell, et al., "Classification Categories and Historical Development of Switching Fabric Topologies," Computing Surveys, Vol. 15, No. 2, June 1983
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- T. G. Robertazzi, Editor, "Performance Evaluation of High Speed Switching Fabrics and Networks," IEEE Press, 1992
- Goke, et al., "Banyan Networks for Partitioning Multiprocessor Systems," 1st Annual Symposium on Computer Architecture, December 1973, pages 21-28
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- Y. Ofek, "Integration Of Voice Communication On A Synchronous Optical Hypergraph", IEEE INFOCOM'88, 1988.
- Li et al., "Time-Driven Priority: Flow Control For Real-Time Heterogeneous Internetworking", IEEE INFOCOM'96, 1996
- Li et al., "Pseudo-Isochronous Cell Forwarding", IEEE INFOCOM'94, pp. 1-19; 1994;
- A. R. Jacob, "A Survey of Fast Packet Switches", Computer Communications Review, January 1990, pages 54-64.
- Y. Ofek, "The Topology, Algorithms And Analysis Of A Synchronous Optical Hypergraph Architecture", Ph.D. Dissertation, Electrical Engineering Department, University of Illinois at Urbana, Report No. UIUCDCS-R-87 1343, May 1987
- A. G. Fraser, "Early Experiment with Asynchronous Time Division Networks", IEEE Networks, pp. 12-26, January 1993

A. Pattavina, "Non-blocking Architecture for ATM Switching", IEEE Communications Magazine, February 1993, pages 37-48

John C. Bellamy, "Digital Network Synchronization", IEEE Communications Magazine, April 1995, pages 70-83.

E. W. Zegura, "Architecture for ATM Switching Systems", IEEE Communications Magazine, February 1993, pages 28- 37

A. Tannebaum, Computer Networks (3rd Ed.) Prentice Hall, 1996

The above-identified patents and documents do not suggest or make obvious the claimed invention. Hence, allowance of the pending application is respectfully requested.

Respectfully submitted,

By: 
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